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7590

06/14/2004

Jack Q. Lever, Jr.
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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/922,671	08/07/2001	Shigeki Furuya	60188-084	1398

TITLE OF INVENTION: CMOS BASIC CELL AND METHOD FOR FABRICATING SEMICONDUCTOR INTEGRATED CIRCUIT USING THE SAME

APPLN. TYPE	SMALL ENTITY	ISSUE FEE	PUBLICATION FEE	TOTAL FEE(S) DUE	DATE DUE
nonprovisional	NO	\$1330	\$300	\$1630	09/14/2004

EXAMINER	ART UNIT	CLASS-SUBCLASS
WARREN, MATTHEW E	2815	257-206000

1. Change of correspondence address or indication of "Fee Address" (37 CFR 1.363).

- ☐ Change of correspondence address (or Change of Correspondence Address form PTO/SB/122) attached.
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 3

3. ASSIGNEE NAME AND RESIDENCE DATA TO BE PRINTED ON THE PATENT (print or type)

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(A) NAME OF ASSIGNEE

(B) RESIDENCE: (CITY and STATE OR COUNTRY)

MATSUSHITA ELECTRIC INDUSTRIAL CO., LTD.

OSAKA, JAPAN

Please check the appropriate assignee category or categories (will not be printed on the patent); ☐ individual ☒ corporation or other private group entity ☐ government

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(Date)

Michael E. Fogarty, Reg. No. 36,139 7/29/04

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07/30/2004 LWONDIH2 00000055 500417 09922671

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 02 FC:1504 300.00 DA
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